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CUSTOMER NO.: 24498
Ser. No. 10/531,220
Office Action dated: 24 June 2009
Response dated: 20 October 2009

PATENT
PU020449

Listing and Amendments to the Claims

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of the Claims

Claim 1 (currently amended): A switch for applying operating power from a peripheral device power source to a peripheral device, the peripheral device being configured for communication with at least one other electronic device by a data bus, the switch including a circuit for sensing ~~communication data~~ signals on said data bus and providing an indication of sensed ~~communication data signals~~ to said peripheral device power source to apply power to said peripheral device in response to said sensed ~~communication data signals~~, said ~~communication~~ sensing circuit comprising:

a transformer having a first winding coupled to said data bus and a second winding coupled to a first switching transistor, wherein the switch has no power dissipation when no activity is present on the data bus.

Claim 2 (previously presented) The switch of claim 1, wherein the first switching transistor is brought from a non-conducting state to a conducting state in the presence of activity on said data bus.

Claim 3 (previously presented): The switch of claim 2, further comprising a second switching transistor having an input coupled to a capacitor, wherein the second switching transistor is brought from a non-conducting state to a conducting state in response to a charge on said capacitor exceeding a threshold level when said first switching transistor is in said conducting state.

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Claim 4 (previously presented): The switch of claim 3, further comprising a capacitor coupled to the output of the second switching transistor for providing an input voltage to a control circuit of the power supply for activating or inactivating the power supply according to the level of the input voltage.

Claim 5 (previously presented): The switch of claim 1, wherein the power supply further includes a latching circuit responsive to initial activation of said power supply for providing a voltage signal to the power supply sufficient to maintain the power supply in an active state independent of the bus activity.

Claim 6 (previously presented): The switch of claim 5, wherein a control input of said power supply is coupled to a controller of said peripheral device for receiving a control signal to cause said power supply to become inactive when there is no activity on said bus.

Claim 7 (previously presented): The switch of claim 1, further comprising means for bypassing said switch to provide a path from a source of input supply voltage to said power supply to cause activation of the power supply independent of bus activity.

Claim 8 (previously presented): The device of claim 6, wherein said controller is configured to provide said control signal to said control input of the power supply after a given time delay based on an absence of bus activity on said data bus.

Claims 9-17 (cancelled)